(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property **Organization**

International Bureau



(43) International Publication Date 26 August 2004 (26.08.2004)

PCT

(10) International Publication Number WO 2004/073273 A1

(51) International Patent Classification⁷:

H04L 12/56

(21) International Application Number:

PCT/KR2004/000320

(22) International Filing Date: 17 February 2004 (17.02.2004)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

03103703.8

17 February 2003 (17.02.2003)

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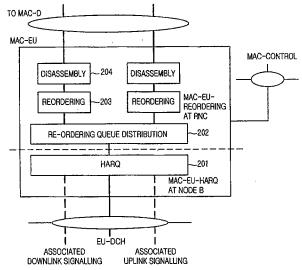
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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,

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(54) Title: HARQ REORDERING METHOD FOR WCDMA ENHANCED UPLINK DEDICATED CHANNEL



(57) Abstract: A method for HARO reordering in Enhanced Uplink Dedicated Channel of WCDMA system includes following steps: locating the soft combination buffer in the Node B and the reordering buffer in the SRNC; locating the HARO entity in the Node B; locating the reordering queue distribution entity, the reordering entity and the disassembly entity in the SRNC. This invention adopts the separation technology of the soft combination buffer and the reordering buffer, the separation model can not only ensure the diversity gain, but also reduce the receiving delay and save the buffer space. Three mechanisms (timer mechanism, window mechanism and SBI mechanism) are proposed to prevent the blocking of the reordering buffer, which degrades the blocking to the least level, improves the receiving efficiency and the system performance. Aiming at the SBI mechanism, the invention proposes the new DCH data frame structure on the Iub interface and the Iur interface.



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TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK,

TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

HARQ REORDERING METHOD FOR WCDMA ENHANCED UPLINK DEDICATED CHANNEL

BACKGROUND OF THE INVENTION

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1. Field of the Invention

This invention relates to a method for Hybrid Automatic Repeat Request reordering in Enhanced Uplink Dedicated Channel in WCDMA (Wideband Code Division Multiple Access) mobile communication system.

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2. Description of the Related Art

Enhanced Up-link Dedicated Channel (simplified as EUDCH hereinafter) is a research project under standardization proposed by version 6 of 3rd Generation Partner Project (3GPP). The purpose of EUDCH is to improve the up-link capacity of the system through efficient management and planning for up-link transmission resources of the wireless network, and to make it applicable to the transmission of data service with high bursts. It improves the percentage of coverage and the throughput of the cell, improves uplink transmission rate as well as reduces uplink delay through improving the performance of uplink dedicated transfer channel. Main technologies adopted are as follows:

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- > Adaptive modulation and coding scheme
- > Hybrid automatic repeat request protocol
- Node B (Base Station) control and scheduling
- Fast dedicated channel set-up
- > Short frame structure and improvement of the quality of service
- Support of relevant physical layer and higher layer signaling

Hybrid Automatic Repeat Request (HARQ) is one of the main technologies. This invention proposes the application method of the HARQ technology in EUDCH with emphasis on the operating method for reordering.

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In the existing WCDMA mobile communication system, the standard about detailed operating method for HARQ in EUDCH doesn't exist yet. In principle, HARQ operation in EUDCH can be regarded as the inverse process of the HARQ operation in High Speed Data Packet Access (HSDPA). In the following, the relevant definition of the HARQ reordering in HSDPA will be explained.

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Figure 1 presents a structure of UE side MAC-hs (Medium Access Control – high speed) entity in HSDPA.

The MAC-hs handles the HSDPA specific functions. In the model below the MAC-hs comprises the following entities:

101 HARQ: The HARQ entity is responsible for handling the MAC functions relating to the HARQ protocol. The HARQ functional entity handles all the tasks that are required for hybrid ARQ. It is responsible for generating ACKs or NACKs. The detailed configuration of the hybrid ARQ protocol is provided by RRC over the MAC-Control SAP.

102 Reordering Queue distribution: The reordering queue distribution function routes the MAC-hs PDUs to the correct reordering buffer based on the Queue ID.

103 Reordering: The reordering entity reorders received MAC-hs PDUs according to the received TSN. MAC-hs PDUs with consecutive TSNs are delivered to the disassembly function upon reception. MAC-hs PDUs are not delivered to the disassembly function if MAC-hs PDUs with lower TSN are missing. There is one reordering entity for each Queue ID configured at the UE.

104 Disassembly: The disassembly entity is responsible for the disassembly of MAC-hs PDUs. When a MAC-hs PDU is disassembled the MAC-hs header is removed, the MAC-d PDUs are extracted and any present padding bits are removed. Then the MAC-d PDUs are delivered to higher layer.

The associated signalling shown in the figure illustrates the exchange of information between layer 1 and layer 2.

At the receiver, there are two buffers, i.e. a soft combining buffer at the physical layer and a re-ordering buffer at the MAC layer. The soft combining buffer is used to combine retransmissions and decode the data. These two buffers are both located in the receiver, i.e. the UE (User Equipment).

If the existing HARQ reordering technology is applied to EUDCH, following two problems may be resulted in:

1) It is not preferable to dispose both soft combining buffer and reordering buffer at the Node B. Considering the case of soft handoff, each Node B in active set is receiving uplink data and the data correctly received by each Node B or not may be different. So it is unnecessary to set a reordering buffer at each Node B, which makes latency too long and results in delay in one aspect, and in another aspect may result in waste of the buffer space. Only if one Node B correctly receives the data, the data can be directly sent to the SRNC (Serving Radio Network Controller) and reordered uniformly at the SRNC, which can obtain the most effective diversity gain.

- 2) In some cases, the reodering buffer can result in perpetual data loss and then result in the queue hole. In conclusion, following several situations may happen:
 - ♦ NACK is understood as ACK by mistake. In this case, the UE won't retransmit this packet but starts to transmit the new packets.
 - ❖ If the retransmission retry of the certain data is too large or the retransmission time reaches at the certain limitation, the UE will give up the retransmission and starts to send new data.
 - ❖ If the transmission of the certain data is pre-empted by other queue with higher priority, the UE will give up the retransmission and starts to send the new data.

If preferable measure isn't adopted to deal with the above several cases, it will result in queue stop, thus correctly received data can't be sent upward and the buffer overflows.

SUMMARY OF THE INVENTION

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The purpose of this invention is to provide a solution with separate models, i.e. separating the soft combining e buffer from the reordering buffer. The soft combining buffer is located in the Node B and the reordering buffer is located in the SRNC respectively. Thus not only the diversity gain of many Node Bs is ensured, but also the queue latency is reduced and the buffer space is saved.

To realize the above purposes, a method for the HARQ reordering in Uplink Channel of telecommunication system includes plurality of Node Bs having soft combine buffers respectively and a RNC having reordering buffer, control each of the Node Bs comprising following steps:

receiving, at the Node Bs, packet data from mobile terminal and storing the received data in the soft combining buffer respectively;

decoding the received data with channel decoder in each Node Bs;

transmitting an Acknowledge or Non-acknowledge information to the mobile terminal;

transmitting, at the Node Bs, the decoded data to the RNC; buffering each decoded data in the reordering buffer; and reordering the decoded data in the RNC.

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To achieve the above purposes, a method for the HARQ reordering in Uplink Channel of telecommunication system includes Node B having soft combining buffer and a RNC having reordering buffer, comprising following steps:

receiving, at the Node B, packet data from mobile terminal and storing the received data in the soft combining buffer;

decoding the received data with channel decoder in Node B;

transmitting an Acknowledge or Non-acknowledge information to the mobile terminal;

transmitting, at the Node B, the decoded data to the RNC;

buffering the decoded data in the reordering buffer; and

reordering the decoded data in the RNC.

To achieve the above purposes, a telecommunication system having hybrid automatic repeat request(HARQ) function, the system comprising;

plurality of Node Bs, at least one of the Node Bs communicates with a terminal, for receiving packet data from a mobile terminal, decoding the packet data and transmitting acknowledgement signal or non-acknowledgement signal to the terminal according to the decoding result;

the mobile terminal for transmitting the packet data and repeating transmission of the packet data when the terminal receive the non-acknowledgement signal; and

a RNC for controlling the Node Bs, receiving decoded data from the Node B, reordering the packet data according to a packet sequence of the packet data,

wherein, the Node B having soft combining buffer buffering the packet data and combining first transmission packet data and second transmission packet data when the Node B receive the packet data again.

The soft combining buffer have two combining method, the first one is chase combining method which combines same packet dada received from first transmission and second transmission. And second another one is IR(incremental redundancy)which combine different version of same packet data.

Aiming at the second problem in the existing technology, this invention proposes three solutions of the operation mechanism.

- a) Timer mechanism
- b) Window mechanism
- c) Soft combination buffer state indicatiing (SBI) mechanism

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These three mechanisms are independent with each other and can be used separately. But in order to reach the optimal performance, the three mechanisms shall be used simultaneously as a combination scheme to maximally eliminate the blocking state and to reach the optimal effect.

The introduction of SBI mechanism needs the support of the new Iub and Iur signaling. This invention proposes an improved DCH data frame structure.

This invention proposes the corresponding MAC-EU (Medium Access Control – Enhanced Uplink) entity structure by adopting the separation technology of the soft combination buffer and the reordering buffer. This separation model can not only ensure the diversity gain, but also reduce the receiving delay and save the buffer space. Three mechanisms are proposed (timer mechanism, window mechanism and SBI mechanism) to prevent the blocking of the reordering buffer, which degrades the blocking to the least level, improves the receiving efficiency and improves the system performance. The invention needs to modify the data frame structure on the Iub interface and the Iur interface to support the new SBI mechanism.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 shows a structure of UE side MAC-hs entity;

Figure 2 shows a structure of a MAC-EU entity at an EUDCH:

Figure 3 exemplifies a blocking state that cannot be solved by the timer—based and window-based mechanisms;

Figure 4 gives a illustration for state shifting of a SBI (soft combination buffer state indicating) according to the example in the Figure 3;

Figure 5 is an operation flowchart for the SRNC reordering based on the timer mechanism;

Figure 6 is an operation flowchart for the SRNC reordering based on the window mechanism;

Figure 7 is an operation flowchart for the SRNC reordering based on the SBI mechanism; and

Figure 8 is an illustration for a structure of a DCH (Dedicated Channel) data frame with the SBI added.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

This invention is consisted of the contents of three aspects.

MAC-EU distributed scheme;

Reordering buffer blocking avoidance scheme; (including three mechanisms, i.e. the timer mechanism, the window mechanism, and the SBI mechanism)

New signaling scheme under the SBI mechanism;

In the following, the contents of each part will be explained in detail:

MAC-EU distributed scheme

In the soft handoff state, several Node Bs in the active set are receiving the uplink data, and the soft combining buffer must be located in the Node B to ensure the diversity gain. The reordering buffer shall be located in the SRNC. After each Node B correctly receives the packet, it will send the packet to the SRNC right away. If there are duplicate data, the SRNC will identify it according to the sequence number (TSN) and discard it automatically. All correctly received packets are queued uniformly in the SRNC.

Accordingly, as illustrated in figure 2, in the MAC-EU entity of EUDCH, the HARQ entity shall be located in the Node B, while the reordering queue distribution entity, the reordering entity and the disassembly entity shall all be located in the SRNC. The detailed structure of the MAC-EU entity (Figure 2) will be explained in the embodiments.

Reordering buffer blocking avoidance scheme

In order to avoid the blocking of the reordering buffer due to the losses of the certain PDUs, new operation mechanism needs to be defined. Here three mechanisms are proposed, which are introduced respectively in the following. These three mechanisms are independent with each other and can be used separately. But in order to reach the optimal performance, the three mechanisms shall be used simultaneously as a combination scheme to maximally eliminate the blocking state and to reach the optimal effect.

1) Timer mechanism

Set a transmission timer (Timer T) at the UE, which is used to control the retransmission time and to prevent the unlimited retransmissions. So the UE can only

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perform retransmission before the termination of the timer. Each time when a new packet is sent, the Timer T is activated; When the UE receives an ACK, the counting stops. If the UE receives a NACK, it retransmits this packet until the timer expires. At this time, the UE gives up the retransmission and starts to send new packets. The time duration of the Timer T is configured by the upper layer.

At the receiver of the SRNC, each queue sets a reordering buffer, and it is the release timer (Timer R) that controls the latency of the empty position. Each time when a MAC-EU PDU is received, if the position with lower sequence number is empty, it cannot be sent upward to the upper layer, and then the Timer R is started up. If the Timer R expires and the position is still empty, it won't wait any more and the position is released. Subsequent data correctly received are delivered to the upper layer. The time duration of the Timer R is configured by the upper layer.

The operation rules for the SRNC reordering buffer under the timer mechanism will be explained in detail in the afterward embodiments (figure 5).

2) Window mechanism

If a window is defined at the UE, the MAC-EU PDU won't result in the confusion of the transmission sequence number (TSN) at the receiver. The size of the transmission window is set as TRANSMIT_WINDOW, whose maximum value is a half of the TSN length range. The upper layer determines the configuration of the TRANSMIT-WINDOW.

After the UE transmits a MAC-EU PDU with TSN=SN, all MAC-EU PDUs satisfying that $TSN \leq SN - TRANSMIT_WINDOW$ cannot be retransmitted, which can prevent the confusion of the sequence number at the receiver. The MAC-EU PDUs that have been given up by the sender won't be retransmitted forever.

If a receiving window is defined at the SRNC, receiving MAC-EU PDU within the window won't result in the window to slide forward. The size of the receiving window is set as RECEIVE_WINDOW, whose maximum value is a half of the TSN length range. The configuration of the RECEIVE_WINDOW is determined by the upper layer.

The operation rules for the SRNC reordering buffer under the window mechanism will be explained in detail in the afterward embodiments (figure 6).

3) Soft combining buffer state indicating (SBI) mechanism

Even employing the timer mechanism and the window mechanism, some blocking cases still cannot be solved. Refer to the example in figure 3: We assume that the TSN is 6-bit long, and so its length range is 64, and the RECEIVE_WINDOW is 32, so the soft combining buffer has 4 processes.

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After the packet with TSN of 2 fails in retransmission about several times, the UE gives up the retransmission and sends the new packets in the same process. At this time, the state of the reordering buffer is shown in the figure. The position with TSN of 2 is empty. The timer R stays in the counting state and the packets with TSN of 3 and 9 are blocked, unable to be sent to the upper layer. The receiving window isn't be pushed forward. In this situation, the SRNC cannot release the reordering buffer in time, and only through waiting for the termination of the timer or the sliding of the window, the position with TSN of 2 can be emptied, which will result in delay and blocking. At this time, if the SRNC knows the state of the soft combination buffer in the Node B, it can quickly release the reordering buffer to prevent the deadlock state. So, Node B needs to send the soft combination buffer state indication bit to the SRNC which can better prevent the blocking after knowing this information.

Each time when the Node B correctly receives a packet, it sends the packet to the SRNC right away. At the same time a SBI bit accompanied is sent in bundling with the data. The SBI bit has two states:

SBI=1: Indicate that all processes in the soft combination buffer are empty, i.e. no data need to be retransmitted.

SBI=0: All other states.

The operation rules for the SRNC reordering buffer under the SBI mechanism will be explained in detail in the afterward embodiments (figure 7).

New signaling scheme under the SBI mechanism.

The introduction of the SBI bit results in the modification to the Iub and Iur data flow protocols. In the following embodiment (figure 8), the uplink (from the Node B to the SRNC) DCH data frame structure on the Iub interface is modified. As the DCH data flow specification on the Iub interface is the same as that on the Iur interface, the data frame structure in figure 8 is also applicable to the Iur interface.

Figure 2 gives the distributed architecture of the MAC-EU entity. From the figure we can see that the HARQ entity is located in the Node B, while the reordering queue distribution entity, the reordering entity and the disassembly entity are all located in the SRNC. The distributed MAC-EU entity is the key precondition for SBI signaling in our invention. In SHO, the distributed MAC-EU scheme means that MAC-EU HARQ entity locating in Node B side and MAC-EU reordering entity locating in SRNC side and this will make system benefit from diversity gain and stall avoidance. Due to multi Node Bs receiving and soft combination separately at respective Node B, diversity gain is obtained. Also because MAC-EU reordering entity is located at SRNC side and SRNC

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receives multi data streams from Node Bs in active set, SRNC will have the ability to disassemble the MAC-EU PDUs faster.

In the model below the distributed MAC-EU comprises of the following entities:

- 201 HARQ: The HARQ entity is responsible for handling the MAC functions relating to the HARQ protocol. The HARQ functional entity handles all the tasks that are required for hybrid ARQ. It is responsible for generating ACKs or NACKs. The detailed configuration of the hybrid ARQ protocol is provided by RRC over the MAC-Control SAP. This entity is located in Node B side.
- 202 Reordering Queue distribution: The reordering queue distribution function routes the MAC-EU PDUs to the correct reordering buffer based on the Queue ID. This entity is located in SRNC side.
- 203 Reordering: The reordering entity reorders received MAC-EU PDUs according to the received TSN. MAC-EU PDUs with consecutive TSNs are delivered to the disassembly function upon reception. MAC-EU PDUs are not delivered to the disassembly function if MAC-EU PDUs with lower TSN are missing. There is one reordering entity for each Queue ID configured at the UE.
- 204 Disassembly: The disassembly entity is responsible for the disassembly of MAC-EU PDUs. When a MAC-EU PDU is disassembled the MAC-EU header is removed, the MAC-d PDUs are extracted and any present padding bits are removed. Then the MAC-d PDUs are delivered to higher layer.

The associated signalling shown in the figure illustrates the exchange of information between layer 1 and layer 2.

Figure 5 is the operation flowchart for the SRNC reordering based on the timer mechanism.

501 Start.

- 502 The MAC-EU PDU with the TSN equaling to the SN is received, but as the position with lower TSN number is empty, it cannot be sent upward.
- 503 Detect whether there is a Timer R activated, and only one Timer R can be in the activate state at one time.
- 504 If no Timer R stays in the activate state at this time, start up the Timer R and the counting.
- 505 If the Timer R expires, detect whether the positions with higher TSN are still empty.

506 If the empty TSN is received before the Timer R expires, stop the Timer R and detect whether the empty positions with higher TSN are still exist.

507 If there is still empty position, all MAC-EU PDUs previous to the next empty position are all sent to the disassembly entity. Then re-start the Timer R at the empty position with the highest TSN and start counting.

508 If no other empty position exists, send all MAC-EU PDUs correctly received to the disassembly entity.

509 End.

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Figure 6 is the operation flowchart for the SRNC reordering based on the window mechanism.

601 Start.

602 The MAC-EU PDU with the TSN equaling to the SN is received.

603 Although the SN is within the receiving window, as the TSN has been received, the current MAC-EU PDU will be discarded.

604 If the SN is within the receiving window and this TSN hasn't been received before, put the MAC-EU PDU onto the corresponding TSN position.

605 If the SN is beyond the window, the SN becomes the highest boundary of the new window and the window slides forward. The MAC-EU PDUs beyond the window will all be sent to the disassembly entity.

606 All MAC-EU PDUs before the first empty position of the window will all be sent to the disassembly entity.

607 End.

Figure 7 is the operation flowchart for the SRNC reordering based on the SBI mechanism.

701 Start.

702 The MAC-EU PDU with the TSN equaling to the SN is received.

703 Detect the SBI bit accompanied, if SBI=1, clear the reordering buffer corresponding to the queue and all MAC-EU PDU received are sent to the disassembly entity.

704 If SBI=0, do nothing.

705 End.

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Figure 4 is the illustration for the SBI state shifting given according to the example in figure 3.

401 The MAC-EU PDU with TSN of 1 is correctly received, and the SBI bit accompanied is 1. As all processes have no data for retransmission at this time, all process buffers are empty.

402 The MAC-EU PDU with TSN of 3 is correctly received, and the SBI bit accompanied is 0. As the process 2 has data for retransmission, the process 2 is not empty.

403 The MAC-EU PDU with TSN of 4 is correctly received, and the SBI bit accompanied is 0. The reason is the same as above.

404 The MAC-EU PDU with TSN of 5 is correctly received, and the SBI bit accompanied is 0. The reason is the same as above.

405 The MAC-EU PDU with TSN of 6 is correctly received, and the SBI bit accompanied is 0. The reason is the same as above.

406 The MAC-EU PDU with TSN of 7 is correctly received, and the SBI bit accompanied is 0. The reason is the same as above.

407 The MAC-EU PDU with TSN of 8 is correctly received, and the SBI bit accompanied is 0. The reason is the same as above.

408 The MAC-EU PDU with TSN of 9 is correctly received, and the SBI bit accompanied is 1. As all processes have no data for retransmission at this time, all process buffers are empty.

Figure 8 shows the DCH data frame structure with the addition of the SBI.

801 Add a SBI bit for each TB in the DCH data frame structure to indicate the state of the soft combination buffer.

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WHAT IS CLAIMED IS:

1. A method for Hybrid Automatic repeat Request reordering in Enhanced Uplink Dedicated Channel of Wideband Code Division Multiple Access system, comprising step of:

locating a soft combination buffer in a Node B and a reordering buffer in a serving radio network controller;

locating a hybrid automatic repeat request entity in the Node B; and

locating a reordering queue distribution entity, a reordering entity and a disassembly entity in the serving radio network controller.

- 2. The method according to Claim 1, wherein setting a transmission timer at a user equipment to control the retransmission time and a release timer at the serving radio network controller to control the waiting time.
- 3. The method according to Claim 1, wherein setting a transmission window at the user equipment to prevent the transmission sequence number confusion at the receiver and a receiving window at the serving radio network controller to control the sliding of the receiving window.
- 4. The method according to Claim 3, wherein the maximum value of said transmission window is a half of the transmission sequence number length range.
- 5. The method according to Claim 3, wherein the maximum value of said receiving window is a half of the transmission sequence number length range.
- 6. The method according to Claim 1, wherein sending a buffer state indication bit accompanied by said Node B at the same time of transmitting a medium access control-enhanced uplink protocol data unit; and

detecting the buffer state indication bit by the serving radio network controller at the same time of receiving the medium access control- enhanced uplink protocol data unit

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and deciding whether to clear the buffer according to the soft combination state indicating.

- 7. The method according to Claim 6, wherein the soft combination buffer state indicating has two states: state 1 indicates that all processes in the soft combination buffer are empty, i.e. no data are needed to be retransmitted; and other cases belong to the state 0.
- 8. The method according to Claim 6, wherein the soft combination buffer state indication bit is added for each transfer block and the data frame structure on an Iub interface is the same as that on an Iur interface.
- 9. A method for the HARQ reordering in Uplink Channel of telecommunication system includes plurality of Node Bs having soft combine buffers respectively and a RNC having reordering buffer, control each of the Node Bs comprising following steps:

receiving, at the Node Bs, packet data from mobile terminal and storing the received data in the soft combining buffer respectively;

decoding the received data with channel decoder in each Node Bs;

transmitting an Acknowledge or Non-acknowledge information to the mobile terminal;

transmitting, at the Node Bs, the decoded data to the RNC; buffering each decoded data in the reordering buffer; and reordering the decoded data in the RNC.

10. A method for the HARQ reordering in Uplink Channel of telecommunication system includes Node B having soft combining buffer and a RNC having reordering buffer, comprising following steps:

receiving, at the Node B, packet data from mobile terminal and storing the received data in the soft combining buffer;

decoding the received data with channel decoder in Node B;

transmitting an Acknowledge or Non-acknowledge information to the mobile terminal;

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transmitting, at the Node B, the decoded data to the RNC; buffering the decoded data in the reordering buffer; and reordering the decoded data in the RNC.

11. A telecommunication system having hybrid automatic repeat request(HARQ) function, the system comprising;

plurality of Node Bs, at least one of the Node Bs communicates with a terminal, for receiving packet data from a mobile terminal, decoding the packet data and transmitting acknowledgement signal or non-acknowledgement signal to the terminal according to the decoding result;

the mobile terminal for transmitting the packet data and repeating transmission of the packet data when the terminal receive the non-acknowledgement signal; and

a RNC for controlling the Node Bs, receiving decoded data from the Node B, reordering the packet data according to a packet sequence of the packet data;

wherein, the Node B having soft combining buffer buffering the packet data and combining first transmission packet data and second transmission packet data when the Node B receive the packet data again.

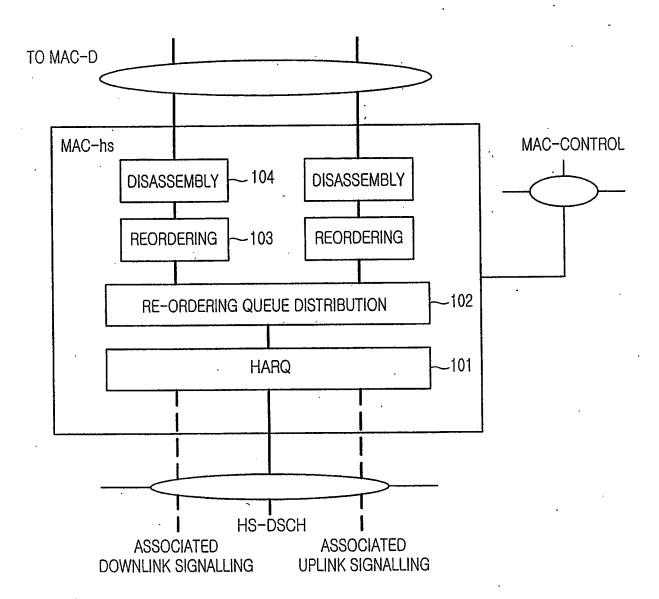


FIG.1

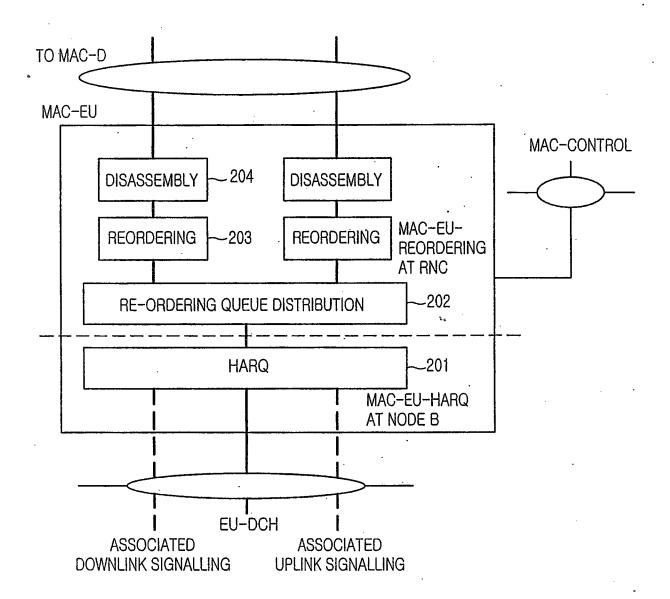


FIG.2

| PROCESS 1 | - | PROCESS 2 | 2 | PROCESS 3 | 3 | PROCESS 4 | 4 |
|-----------|--------|-------------------|----------|-----------|-------------|-----------|-----|
| TSN=1 | ACK | | NACK | TSN=3 | ACK | TSN=4 | ACK |
| TSN=5 | ACK | | NACK | TSN=6 | ACK | TSN=7 | ACK |
| TSN=8 | ACK | TSN=9 | ACK | | | | |
| | | | | | | | |
| | REORDE | REORDERING BUFFER | 6 | | | | - 1 |
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| | | 4 | | | | | |
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FIG.3

| 401 | MAC-EU PDU TSN = 1 (SBI = 1) |
|-----|------------------------------|
| | |
| 402 | MAC-EU PDU TSN = 3 (SBI = 0) |
| | - |
| 403 | MAC-EU PDU TSN = 4 (SBI = 0) |
| • | ↓ |
| 404 | MAC-EU PDU TSN = 5 (SBI = 0) |
| | |
| 405 | MAC-EU PDU TSN = 6 (SBI = 0) |
| | 1 |
| 406 | MAC-EU PDU TSN = 7 (SBI = 0) |
| | ↓ |
| 407 | MAC-EU PDU TSN = 8 (SBI = 0) |
| | \ |
| 408 | MAC-EU PDU TSN = 9 (SBI = 1) |

FIG.4

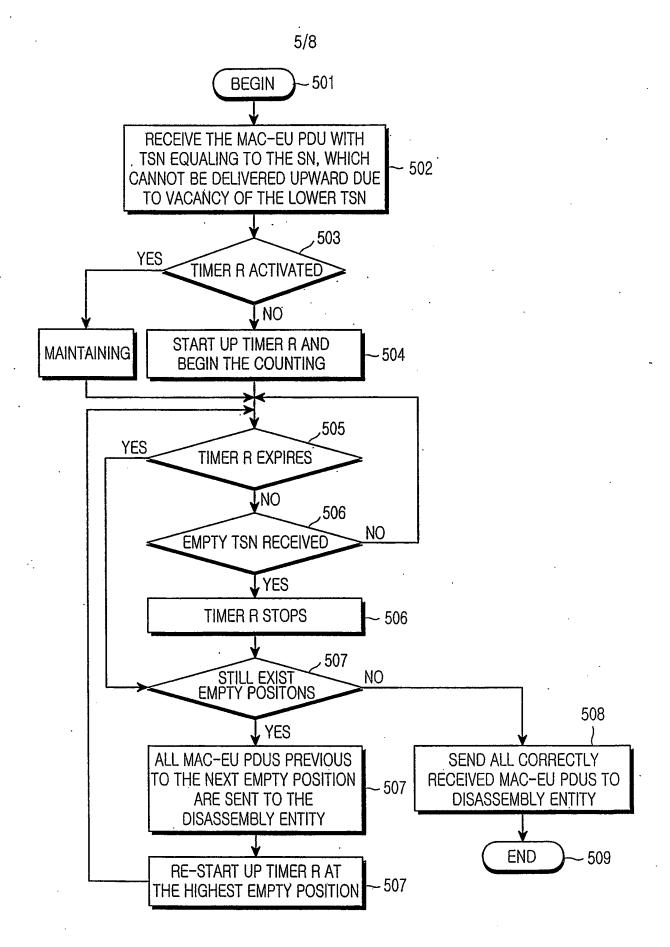


FIG.5

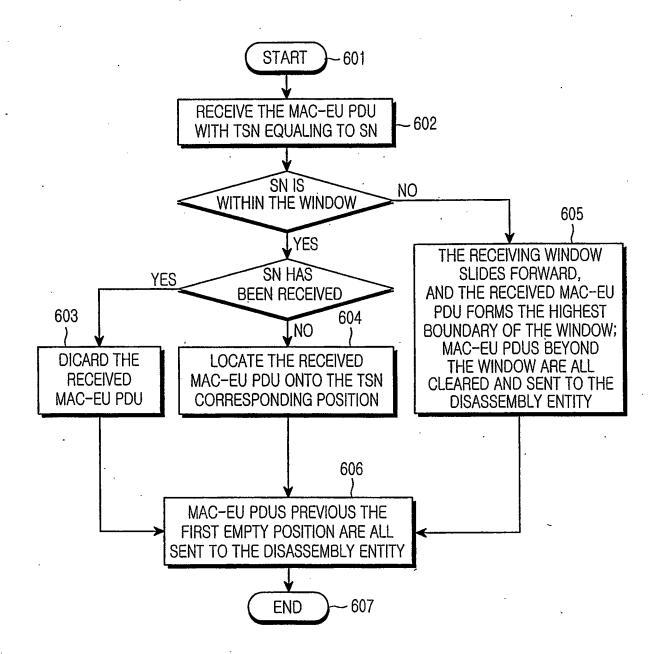


FIG.6

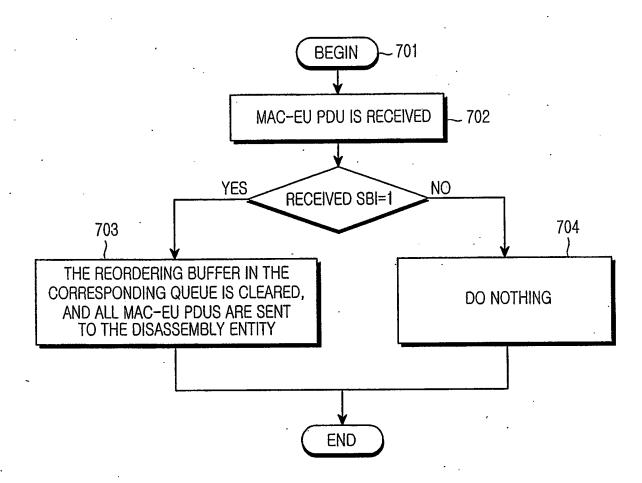


FIG.7

| HEADER CRC | | | FRAM | E TYPE (FT) | [] | |
|-----------------------------|-----------|-------------|----------------------|---------------------------------------|---------------------------------------|-----------|
| CFN | | | | | | |
| SPARE TFI OF FIRST DCH | | | | | | |
| | | - | | | | |
| SPARE TFI OF LAST DCH . | | | | | | |
| | FIR | ST TB OF F | FIRST DCH | | | |
| | | | | | | |
| FIRST | TB OF | FIRST DC | H (CONT.) | | PAD | |
| | | | | | | |
| | LAS | T TB OF FI | IRST DCH | | | |
| | | | NI (OONT | , | | |
| LAS | | F FIRST DO | CH (CONT. | | PAD | |
| | CII | חסד דם סכ | I ACT DOL | 1 | | |
| | | RST TB OF | LASI DUF | 1 | | |
| EIDOT | TRO | F LAST DCI | H (CONT) | - · · - · · · | PAD | |
| | | | | | | |
| • | ΙΔ9 | ST TB OF L | AST DCH | | | DAVLOAD |
| | | | | | | > PAYLOAD |
| LAST TB OF LAST DCH (CONT.) | | | | · | PAD | |
| | | | QE | | <u> </u> | |
| CRCI OF FI | RST | | · | | · · · · · · · · · · · · · · · · · · · | |
| TB OF FIRST | DCHT | | _ | i- | | |
| | | | CRCI OF TB OF LAS | | PAD | · |
| SBI OF FIRS | ST TB | | 1001010 | 1 0011 | | <u> </u> |
| OF FIRST E | UDCH | | l L | · 1 | | |
| | <u>-</u> | | SBI OF LA | ST TR | | 801 |
| | | | OF LAST E | UDCH! | PAD | 丿 |
| | | | XTENSION | · · · · · · · · · · · · · · · · · · · | | `_ |
| | | | AD CRC | | | OPTIONAL |
| PAYLOAD CRC (CONT.) | | | | | |]]]] |

FIG.8

INTERNATIONAL SEARCH REPORT

iternational application No.

| | THE TANK TO THE OFFICE OF | CEI OICI | DCT//CD2004/ | 04/000320 | | |
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| A CLAS | COLETC VALUE OF CHILD INC. AND ASSESSED | | PC1/KR2004/ | 000320 | | |
| | SSIFICATION OF SUBJECT MATTER | | | | | |
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| | umentation searched (classification system followed by | y classification symbols) | | | | |
| IPC7 H04J, I | H04L, H04Q | | | | | |
| Demonstration | | | | | | |
| Korean Pater | n searched other than minimum documentation to the ents and applications for inventions since 1975 by models and applications for Utility models since 197 | | included in the f | ields searched | | |
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| Electronic data eKIPASS | a base consulted during the intertnational search (name | of data base and, where practic | cable, search terr | ns used) | | |
| C. DOCUM | MENTS CONSIDERED TO BE RELEVANT | | | | | |
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| "A" document to be of pa "E" earlier app filing date "L" document cited to es special re: "O" document means "P" document | ategories of cited documents: defining the general state of the art which is not considered articular relevance plication or patent but published on or after the international which may throw doubts on priority claim(s) or which is stablish the publication date of citation or other ason (as specified) referring to an oral disclosure, use, exhibition or other published prior to the international filing date but later riority date claimed | "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family | | | | |
| Date of the act | ual completion of the international search | Date of mailing of the interna | tional search rep | oort | | |
| 09 | 9 APRIL 2004 (09.04.2004) | 09 APRIL 2004 (0 | 9.04.2004) | | | |
| | iling address of the ISA/KR | Authorized officer | ··- <u>-</u> - | | | |
| 5-4- | Korean Intellectual Property Office 920 Dunsan-dong, Seo-gu, Daejeon 302-701, Republic of Korea | KIM, Jong Hwa | | | | |

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